



# DVPL0510S Series

## HIGH RELIABILITY HYBRID DC-DC CONVERTERS

### DESCRIPTION

The DVPL series of high reliability DC-DC converters is operable over the full military (-55 °C to +125 °C) temperature range with no power derating. Operating at a nominal fixed frequency of 300 kHz, these regulated, non-isolated converters are optimized for low voltage point of load applications with high efficiency synchronous rectification, fast transient response, and power sequencing capability.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

This product may incorporate one or more of the following U.S. patents:

5,784,266  
5,790,389  
5,963,438  
5,999,433  
6,005,780  
6,084,792  
6,118,673

### FEATURES

- High Reliability
- Operates From 5V Input
- Adjustable Output Voltage, 0.8V to 3.4V
- Up to 10 Amps or 33W Output
- High Efficiency, Up to 96%
- High Power Density: >100 W/in<sup>3</sup>
- Output Voltage Sequencing
- Output Voltage Tracking
- Operation Into a Pre-Biased Output
- Output Inhibit Control
- Low Output Noise
- NO Use of Optoisolators
- Over Current and Short Circuit Protection
- Precision Projection Welded Hermetic Package
- Meets MIL-STD-461C and MIL-STD-461D EMC Requirements When Used With VPT DC-DC Converters and EMI filters
- Additional Environmental Screening Available
- MIL-PRF-38534 Element Evaluated Components



**Figure 1** – DVPL0510S DC-DC Converter  
(Exact marking may differ from that shown)

## SPECIFICATIONS (T<sub>CASE</sub> = -55°C to +125°C, V<sub>IN</sub> = +5V ± 5%, Full Load, Unless Otherwise Specified)

### ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous)	5.5 V <sub>DC</sub>	Junction Temperature Rise to Case	+15°C
Input Voltage (Transient, 1 second)	5.8 V <sub>DC</sub>	Storage Temperature	-65°C to +150°C
Output Power <sup>1</sup>	33 Watts	Lead Solder Temperature (10 seconds)	270°C
Power Dissipation (Full Load, T <sub>CASE</sub> = +125°C)	3.3 Watt	Weight (Maximum)	16 Grams
ESD Rating per MIL-PRF-38534	3A		

Parameter	Conditions	DVPL0510S			Units	
		Min	Typ	Max		
<b>STATIC</b>						
INPUT Voltage <sup>4</sup>	V <sub>IN</sub>	Continuous	4.5	-	5.5	V
		Transient	-	-	5.8	V
Current		Inhibited	-	-	3	mA
		No Load	-	-	80	mA
Inhibit Pin Input <sup>4</sup>			0	-	1.5	V
Inhibit Pin Open Circuit Voltage <sup>4</sup>			-	-	V <sub>in</sub>	V
UVLO Turn On <sup>4,6</sup>			-	2.2	-	V
UVLO Turn Off <sup>4</sup>			-	2.0	-	V
OUTPUT Voltage	V <sub>OUT</sub>	T <sub>CASE</sub> = 25°C	-1.5	V <sub>out</sub>	+1.5	%V <sub>out</sub>
	V <sub>OUT</sub>	T <sub>CASE</sub> = -55°C to +125°C	-2.5	V <sub>out</sub>	+2.5	%V <sub>out</sub>
Power <sup>3,1</sup>		V <sub>OUT</sub> = 3.3V	0	-	33	W
Current <sup>3</sup>	V <sub>OUT</sub>		0	-	10	A
Ripple Voltage	V <sub>OUT</sub>	Full Load, 20Hz to 10MHz	-	40	80	mV <sub>p-p</sub>
Load Regulation	V <sub>OUT</sub>	No Load to Full Load	-	25	60	mV
EFFICIENCY		V <sub>out</sub> = 3.3V	91	95	-	%
FAULT POWER DISSIPATION		Short Circuit	-	-	3	W
CAPACITIVE LOAD <sup>4</sup>			-	-	5000	μF
SWITCHING FREQUENCY			240	300	350	kHz
CASE ISOLATION		500 V <sub>DC</sub>	100	-	-	MΩ
MTBF (MIL-HDBK-217F)		AIF @ T <sub>C</sub> = 55°C	-	562	-	kHrs
<b>DYNAMIC</b>						
Load Step Output Transient <sup>5</sup>	V <sub>OUT</sub>	Half Load to Full Load	-	150	300	mV
Load Step Recovery <sup>2,5</sup>		V <sub>out</sub> = 3.3V	-	70	160	μSec
Turn On Delay	V <sub>OUT</sub>	V <sub>IN</sub> = 0V to 5V	-	-	8	mSec
Turn On Overshoot			-	-	40	mV <sub>PK</sub>
<b>VOLTAGE TRACKING</b>						
Tracking Accuracy <sup>4</sup>		Power-up	-	100	300	mV
		Power-down	-	200	400	mV
Slew Rate <sup>4</sup>		At track pin	0.1	-	2	V/mSec
Tracking Delay <sup>4</sup>		From V <sub>in</sub> to tracking voltage applied	10	-	-	mSec

1. Dependent on output voltage.
2. Time for output voltage to settle within 1% or 20mV of its nominal value, whichever is greater.
3. Derate linearly to 0 at 135°C.
4. Verified by initial electrical design verification. Post design verification, parameter shall be guaranteed to the limits specified.
5. With 100μF capacitor from V<sub>in</sub> to Ground.
6. V<sub>out</sub> not necessarily in regulation.

**BLOCK DIAGRAM**

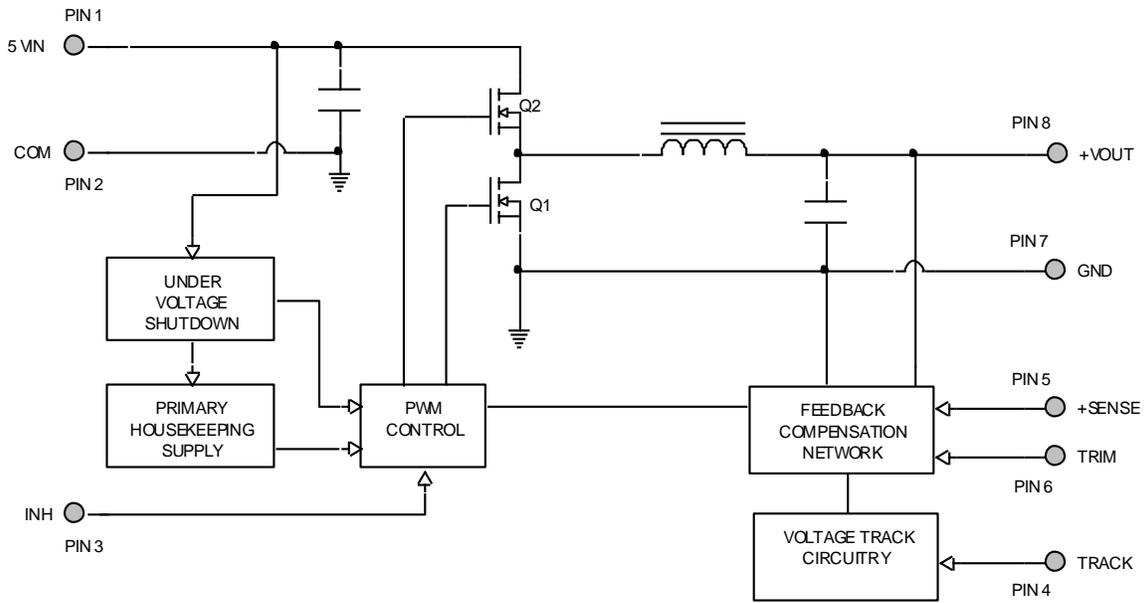


Figure 2

**CONNECTION DIAGRAM**

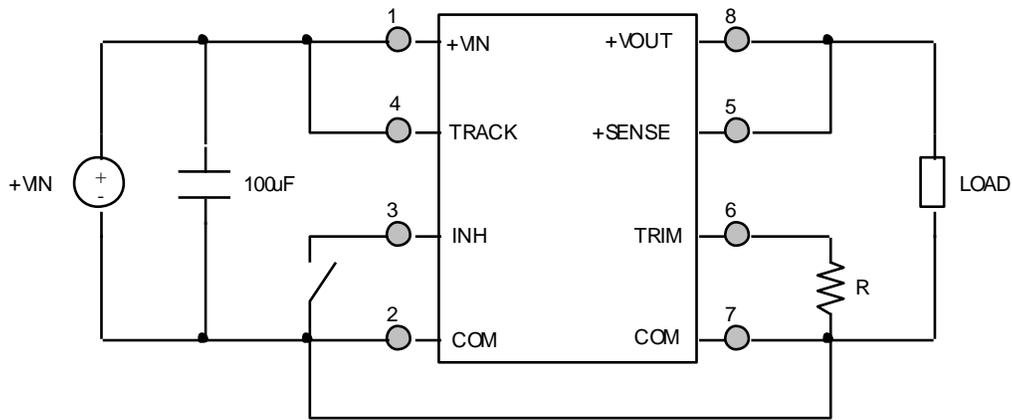


Figure 3  
(Shown with recommended 100uF input capacitor)

**TYPICAL DISTRIBUTED APPLICATION**

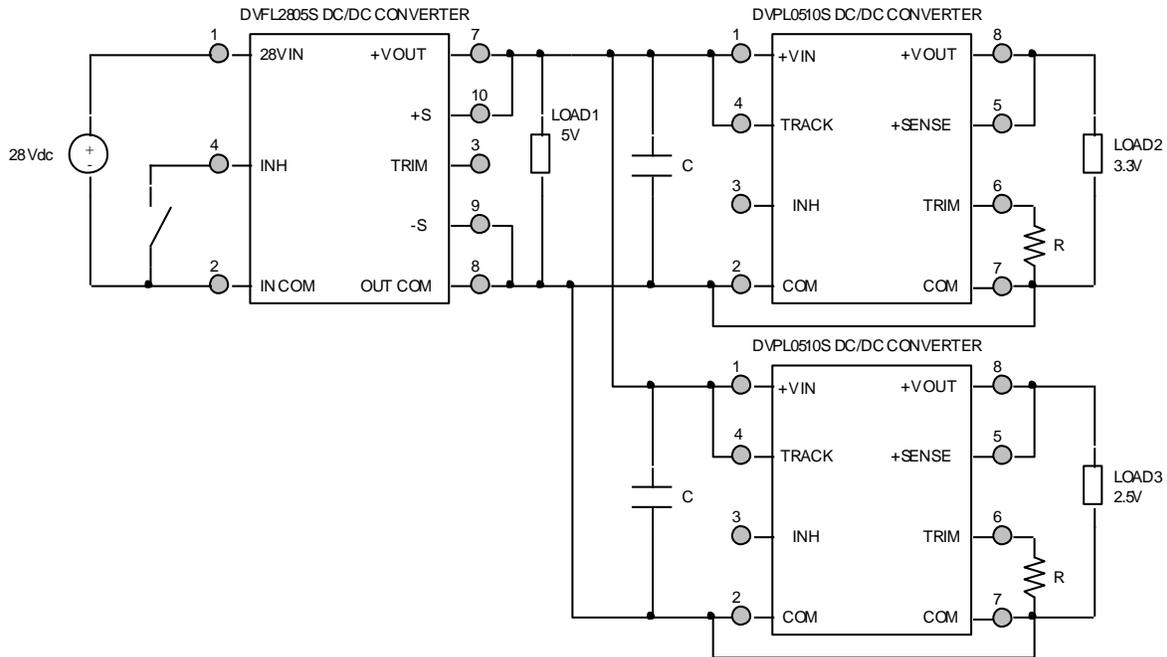


Figure 4

**TRACKING APPLICATION**

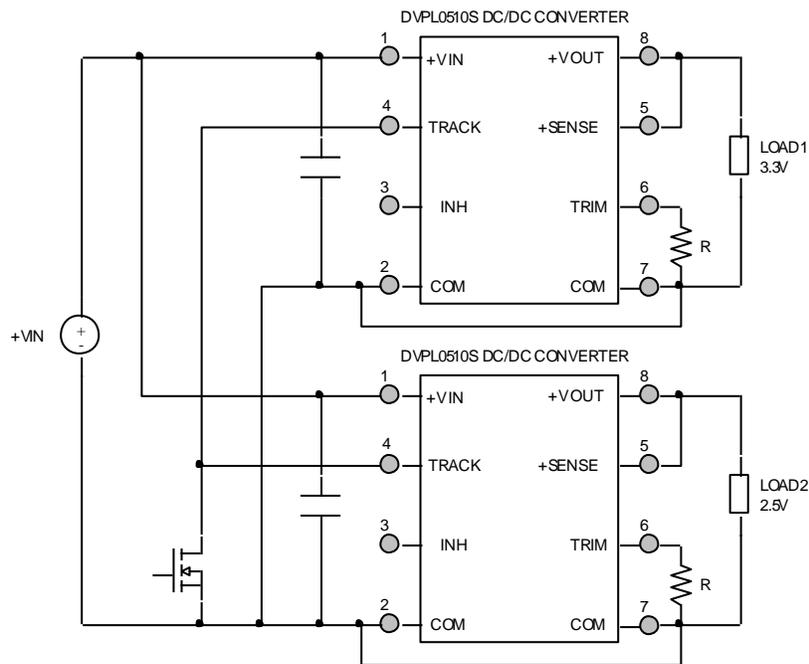
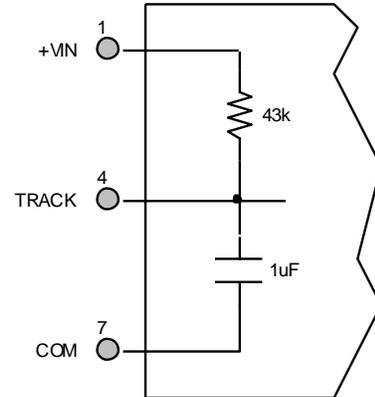
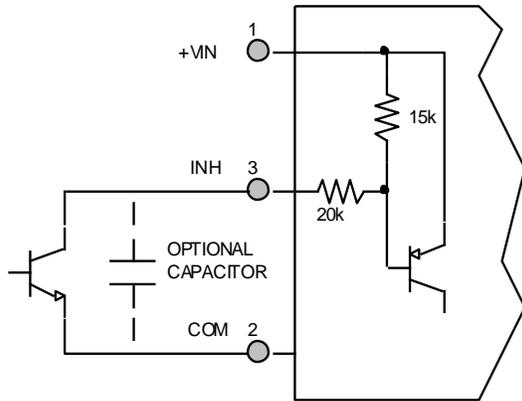


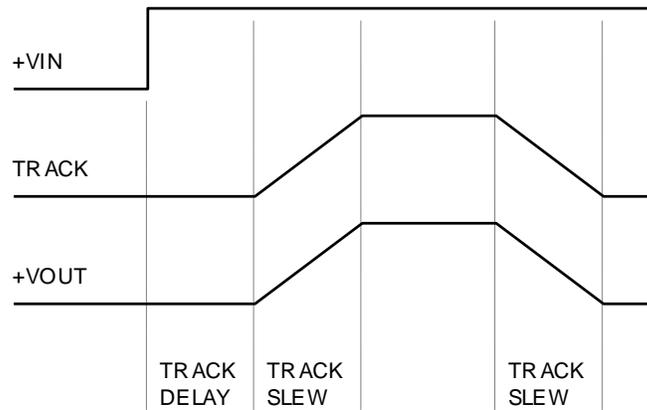
Figure 5

**INTERNAL CONNECTION DIAGRAMS**



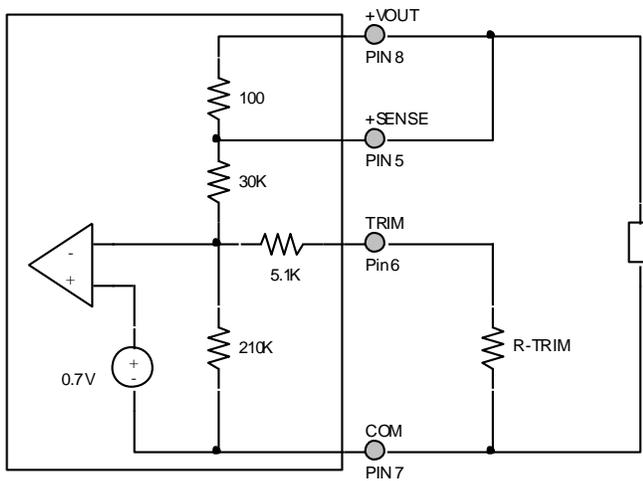
**Figure 6** – Internal Inhibit Circuit and Recommended Drive  
(Shown with optional capacitor for turn-on delay)

**Figure 7** – Internal Track Circuit



**Figure 8** – Track timing relations

**OUTPUT VOLTAGE TRIM**



The output voltage is set with an external resistor connected from the TRIM pin to the GND pin 7. This resistor must have a tolerance of 1% or less to achieve an accurate output voltage.

The default output voltage with the TRIM pin left open is 0.80V.

DVPL0510S	
+V <sub>OUT</sub> (V)	R <sub>TRIM</sub> (Ω)
0.8	-
0.9	205k
1.0	99.9k
1.2	47.4k
1.5	24.9k
1.8	15.9k
1.9	14k
2.0	12.4k
2.5	7.25k
2.8	5.40k
3.0	4.44k
3.3	3.30k
3.4	2.98k

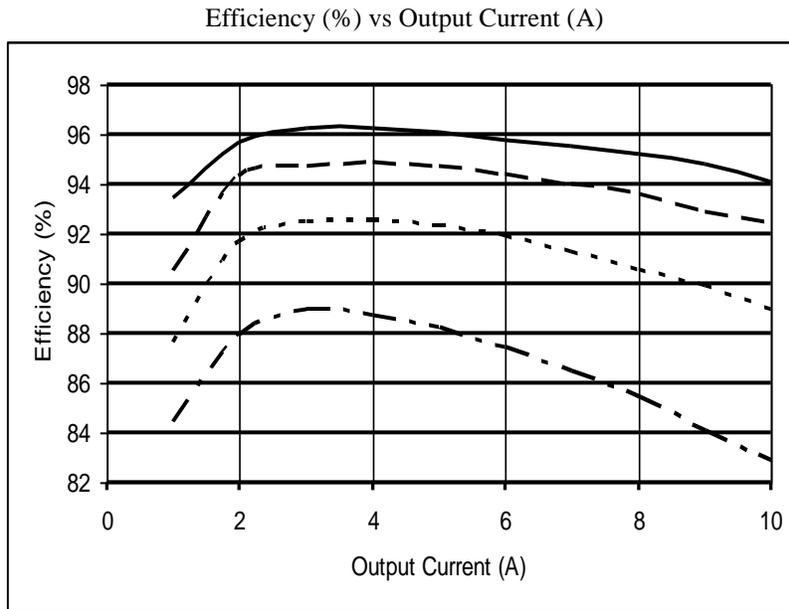
$$V_O = \frac{168R_{TRIM} + 5.267 * 10^6}{210R_{TRIM} + 1.071 * 10^6}$$

$$R_{TRIM} = \frac{5.267 * 10^6 - 1.071 * 10^6 * V_O}{210 * V_O - 168}$$

**Figure 9** – Output Voltage Trim

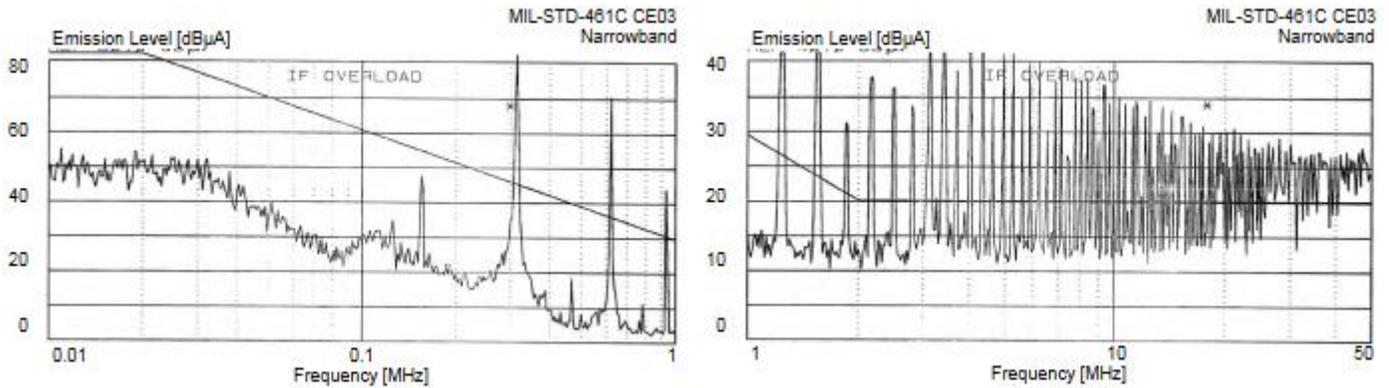
**EFFICIENCY PERFORMANCE CURVES** ( $T_{CASE} = 25^{\circ}C$ , Full Load, Unless Otherwise Specified)

——— $V_{OUT} = 3.3V$	- - - - - $V_{OUT} = 2.5V$	- · - · - $V_{OUT} = 1.5V$	- · - · - $V_{OUT} = 0.8V$
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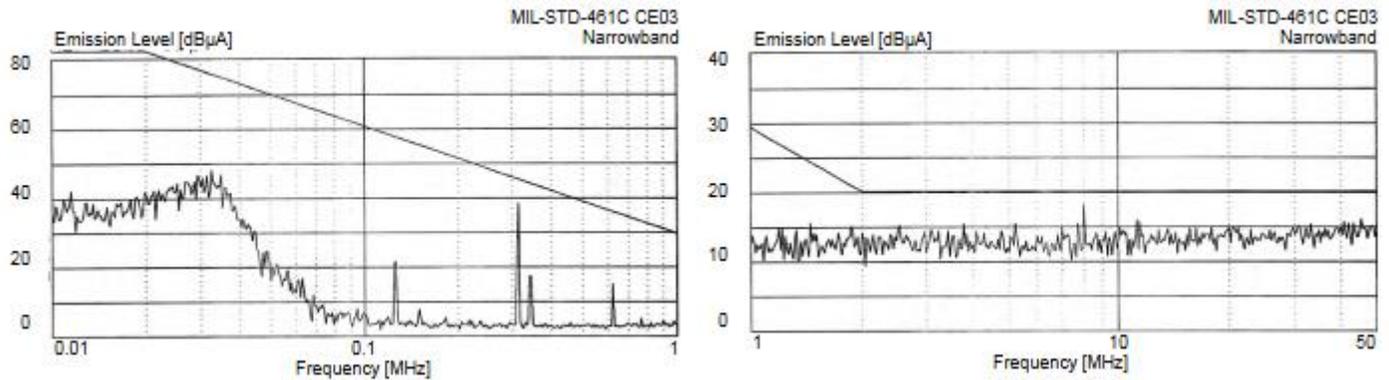


**Figure 10** – DVPL0510S  $V_{in}=5.0V$   
Efficiency (%) vs. Output Current (A)

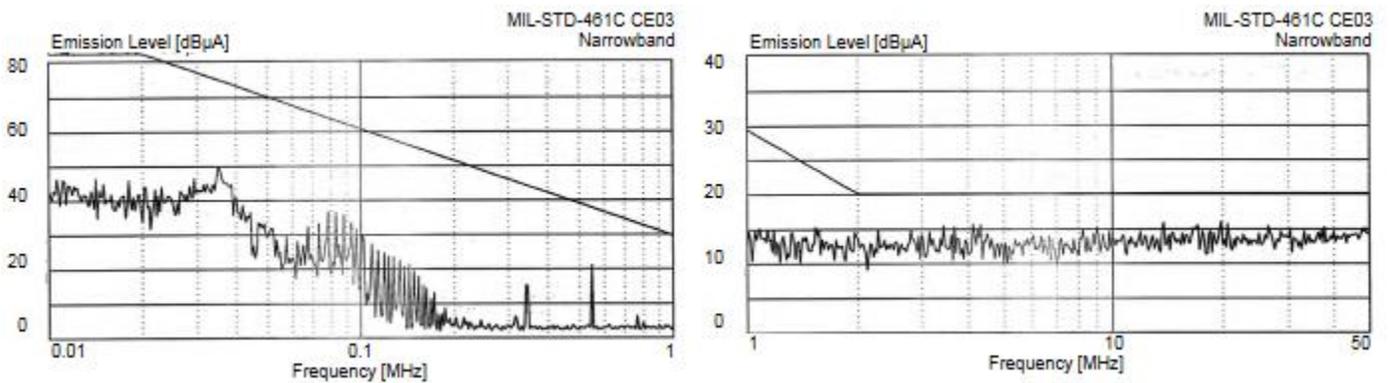
**EMI PERFORMANCE CURVES** (T<sub>CASE</sub> = 25°C, Full Load, Unless Otherwise Specified)



**Figure 11** – 5V input line, DVPL, C<sub>IN</sub>=100μF

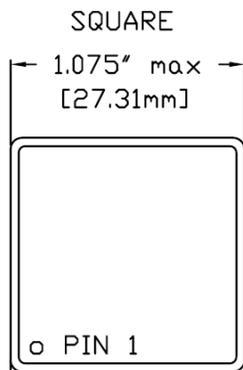


**Figure 12** – 5V input line, DVMD and DVPL, C<sub>IN</sub>=100μF

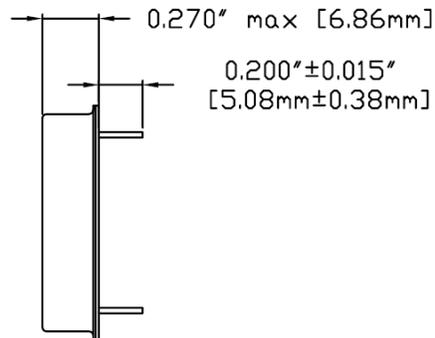


**Figure 13**- 28V input line, DVME, DVFL and DVPL, C<sub>IN</sub>=100μF

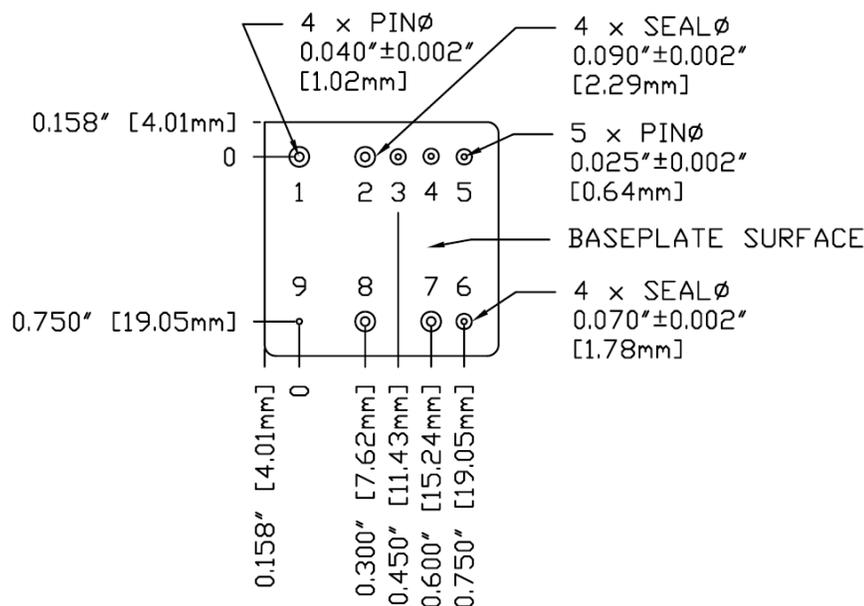
**PACKAGE SPECIFICATIONS**



TOP VIEW



SIDE VIEW



BOTTOM VIEW

NOTES:

1. DIMENSIONAL LIMITS ARE ±0.005" UNLESS OTHERWISE STATED.
2. CASE TEMPERATURE IS MEASURED ON THE CENTER OF THE BASEPLATE.
3. MATERIALS:  
CASE: STEEL, GOLD OVER NICKEL PLATED.  
COVER: STEEL, NICKEL PLATED.  
PINS: COPPER CORED ALLOY 52 (0.040"Ø), ALLOY 52 (0.025"Ø), GOLD OVER NICKEL PLATED.  
PIN SEALS: GLASS

Pin	Function	Pin	Function	Pin	Function
1	+V IN	4	TRACK	7	COM
2	COM	5	+SENSE	8	+V OUT
3	INHIBIT	6	TRIM	9	CASE

Figure 14 – Package and Pinout

## PACKAGE PIN DESCRIPTION

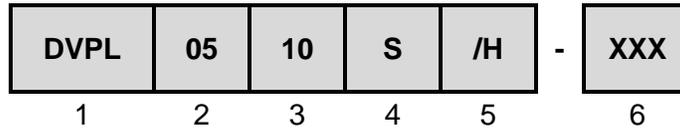
Pin	Function	Description
1	+VIN	Positive Input Voltage Connection
2	COM	Common Return Connection, Input Side
3	INHIBIT	Logic Low = Disabled Output. Connecting the inhibit pin to common causes converter shutdown. Logic High (open collector or open drain) = Enabled Output. Leave open if not used.
4	TRACK	The output voltage will follow this pin. Used for output voltage sequencing or tracking. If not used, the Track pin should be connected to Vin.
5	+SENSE	Positive Sense
6	TRIM	Trim pin sets output voltage with a resistor to pin 7
7	COM	Common Return Connection, Output Side
8	+VOUT	Positive Output Voltage Connection
9	CASE	Case Connection

## ENVIRONMENTAL SCREENING (100% Tested Per MIL-STD-883 as referenced to MIL-PRF-38534)

Test	MIL-STD-883 Test Method, Condition	No Suffix (Standard) Non-QML ⑤	/ES (Extended) Non-QML ⑤	/H (Class H)	/K (Class K)
Non-Destructive Bond Pull	TM2023	• ④	• ④	• ④	•
Internal Visual	TM2010, TM2017, TM2032 (MIL-STD-750, TM2072, TM2073)	•	•	•	•
Temperature Cycling	TM1010, Condition C -65°C to 150°C, Ambient			•	•
	TM1010, Condition B -55°C to 125°C, Ambient		•		
Constant Acceleration	TM2001, 3000g, Y1 Direction			•	•
	TM2001, 500g, Y1 Direction		•		
PIND ⑦	TM2020, Condition A				•
Pre Burn-In Electrical	25°C				•
Burn-In	TM1015, 320 hrs, 125°C, Case Typ				•
	TM1015, 160 hrs, 125°C, Case Typ			•	
	96 hrs, 125°C, Case Typ		•		
	24 hrs, 125°C, Case Typ	•			
Final Electrical	MIL-PRF-38534, Group A Subgroups 1-6 -55°C, 25°C, 125°C ③			•	•
	MIL-PRF-38534, Group A Subgroups 1 and 4 25°C	•	•		
Hermeticity (Seal)	TM1014, Fine Leak, Condition A2 or B1		•	•	•
	TM1014, Gross Leak, Condition C or B2		•	•	•
	Gross Leak, Dip (1 x 10 <sup>-3</sup> )	•			
Radiography ⑧	TM2012				•
External Visual	TM2009	•	•	•	•

- Notes:
- ① Contact Sales for more information concerning additional environmental screening and testing options desired.
  - ② VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.
  - ③ 100% R&R testing with all test data included in product shipment.
  - ④ Not required per MIL-PRF-38534. Test is performed for additional product quality assurance.
  - ⑤ Non-QML products may not meet all requirements of MIL-PRF-38534.
  - ⑥ Note intentionally not used.
  - ⑦ PIND test Certificate of Compliance included in product shipment.
  - ⑧ Radiographic test Certificate of Compliance and film(s) or data CD included in product shipment.

## ORDERING INFORMATION



(1) Product Series	(2) Nominal Input Voltage		(3) Output Current		(4) Number of Outputs	
DVPL	05	5 Volts	10	10 Amps	S	Single

(5) Screening Code <sup>1,2</sup>		(6) Additional Screening Code
None /ES /H /K	Standard Extended Class H Class K	Contact Sales

- Notes:
1. Contact the VPT Inc. Sales Department for availability of Class H (/H) or Class K (/K) qualified products.
  2. VPT Inc. reserves the right to ship higher screened or SMD products to meet lower screened orders at our sole discretion unless specifically forbidden by customer contract.

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.

## SMD (STANDARD MICROCIRCUIT DRAWING) NUMBERS

Standard Microcircuit Drawing (SMD)	DVPL0510S Series Similar Part Number
5962-1321901HXC	DVPL0510S/H
5962-1321901HXA	DVPL0510S/H-E
5962-1321901KXC	DVPL0510S/K
5962-1321901KXA	DVPL0510S/K-E

Do not use the DVPL0510S Series similar part number for SMD product acquisition. It is listed for reference only. For exact specifications for the SMD product, refer to the SMD drawing. SMDs can be downloaded from the DLA Land and Maritime (Previously known as DSCC) website at <https://landandmaritimeapps.dla.mil/programs/defaultapps.asp>. The SMD number listed above represents the Federal Stock Class, Device Type, Device Class Designator, Case Outline, Lead Finish and RHA Designator (where applicable). Please reference the SMD for other screening levels, lead finishes, and radiation levels. All SMD products are marked with a "Q" on the cover as specified by the QML certification mark requirement of MIL-PRF-38534.

## CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

**Phone:** (425) 353-3010  
**Fax:** (425) 353-4030  
**E-mail:** [vptsales@vptpower.com](mailto:vptsales@vptpower.com)

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